

# **Sympiler: Transforming Sparse Matrix Codes by Decoupling Symbolic Analysis**

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# OUTLINE

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- Overview
- Sympiler: A code generator for optimizing sparse matrix methods
  - Sympiler internals (input, inspection, transformation, and code generation) with the triangular system solver example
  - Sympiler for Cholesky factorization
- Conclusion

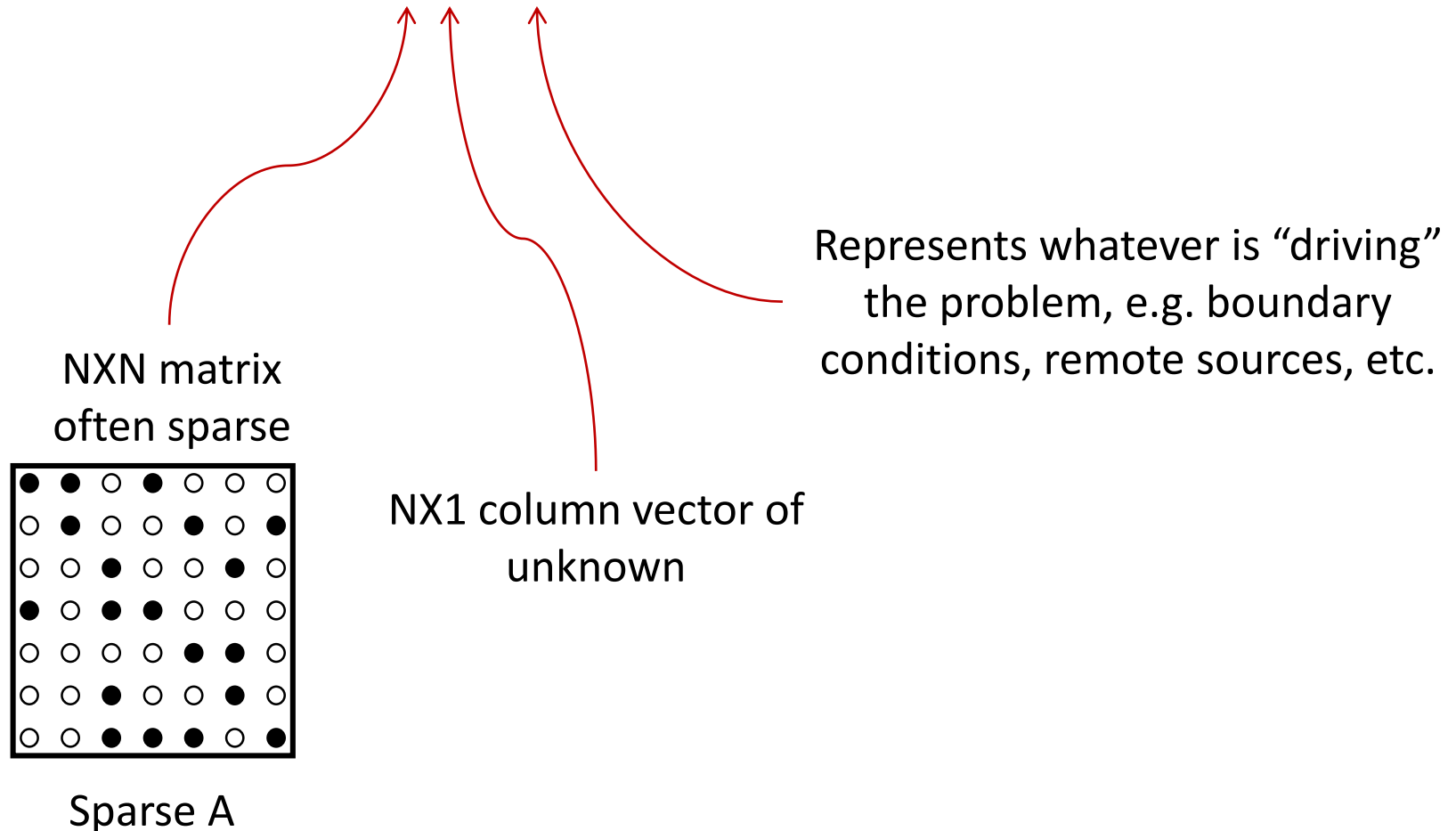
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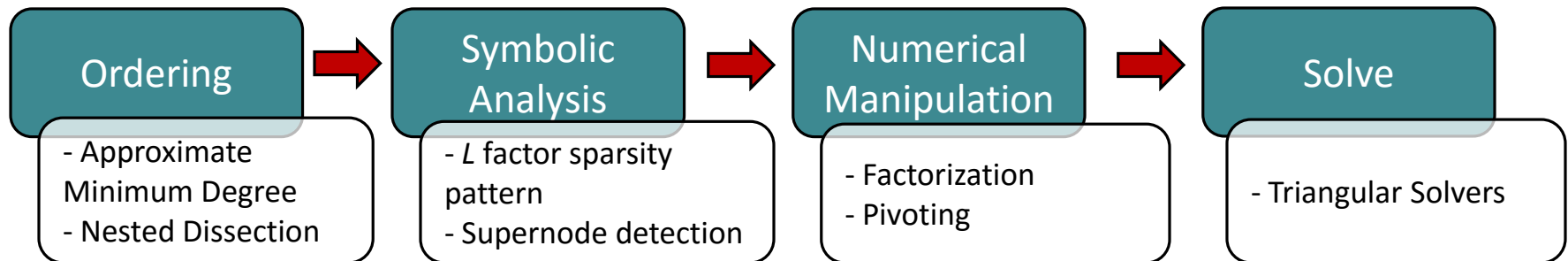
# LINEAR SYSTEMS OF EQUATIONS

Many simulations in scientific problems require solving a **linear system of equations**,  $Ax = b$ .



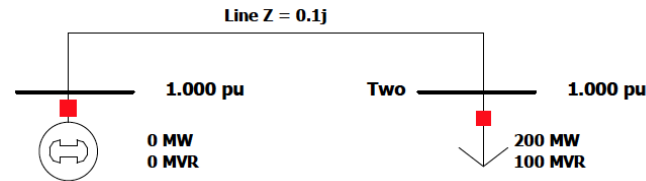
# SYMBOLIC ANALYSIS IN SPARSE MATRIX SOLVERS

- Linear system solver classification:
  - Direct solvers
  - Iterative solvers
- Steps to factorizing a sparse matrix and solving a linear system using a direct method: Cholesky ( $A=LL^T$ ), triangular solver ( $Lx=b$ )

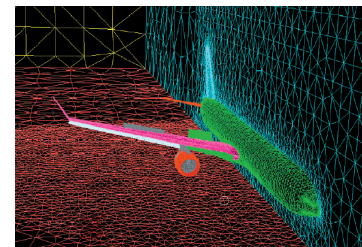
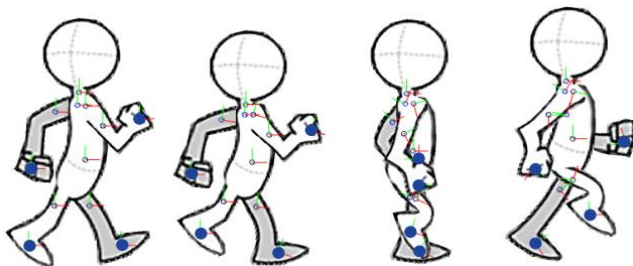
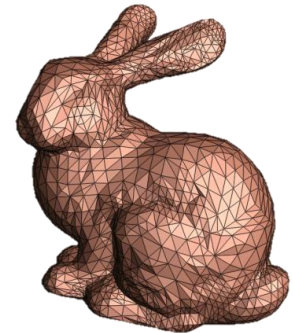


# STATIC SPARSITY PATTERNS

The **sparsity patterns** in many applications such as power modeling, animation, and circuit simulation often remain **static** for a period of time since the structure arises from the physical topology of the underlying system, the discretization, and the governing equations.



**Sympiler** generates optimized code for a static sparsity pattern by doing symbolic analysis at compile-time.



# LIBRARY VS. SYMPILER


## CODES FOR SPARSE TRIANGULAR SOLVE

### Library

```
int top=Reach(Lp,Li,x,stack);
for (px=top; px>0; px--) {
    j=stack[px];
    x[j]/=Lx[Lp[j]]
    p=Lp[j]+1;
    for (; p<Lp[j+1]; p++)
        x[Li[p]]-=Lx[p]*x[j];
}
```

Symbolic analysis coupled with the numerical code

### Sympiler



```
x=b;
x[0] /= Lx[0]; // Peel
for(p = 1; p < 3; p++)
    x[Li[p]] -= Lx[p] * x[0];

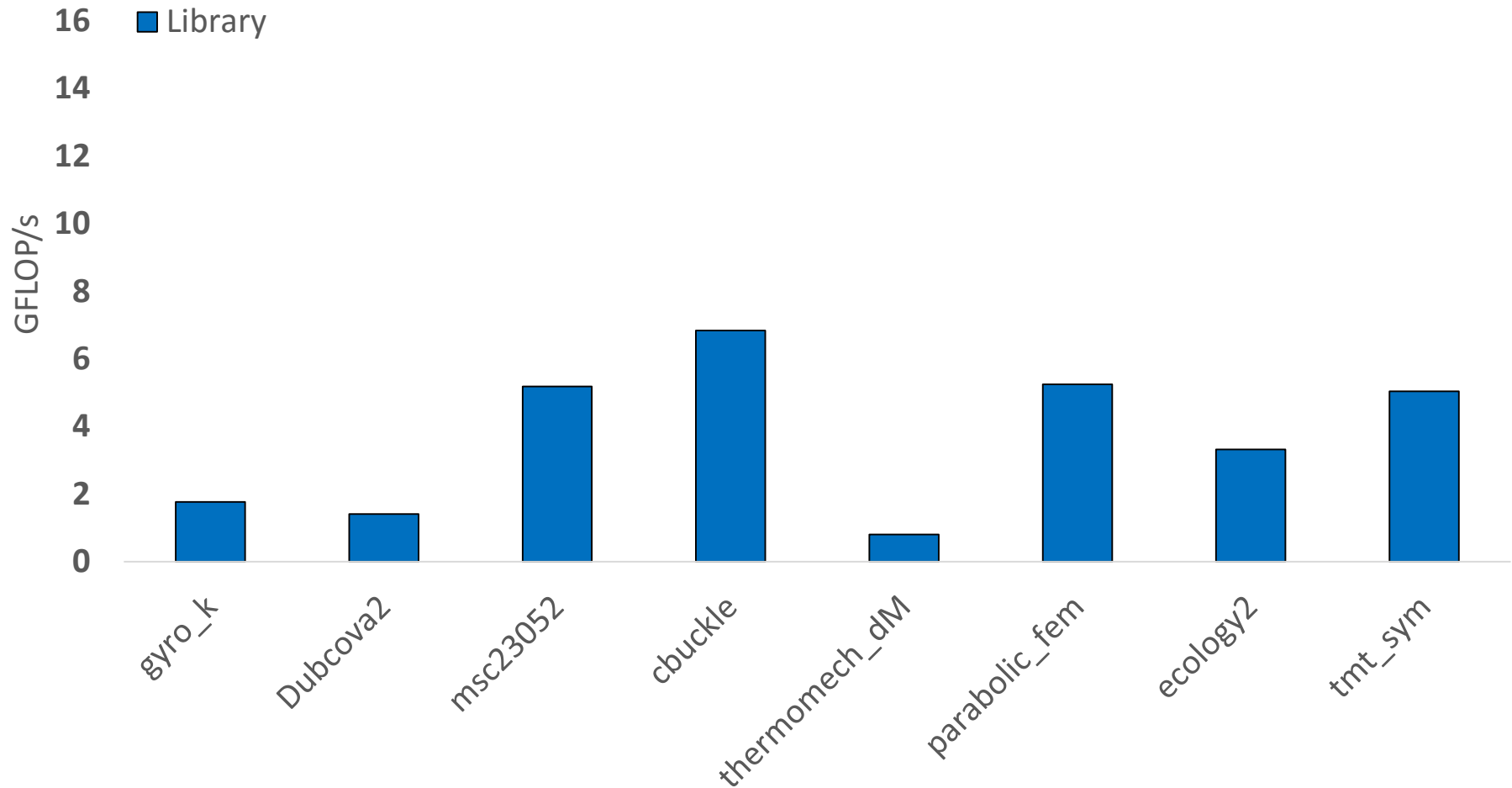
for(px=1;px<3;px++){
    j=reachSet[px];x[j]/=Lx[Lp[j]];
    for(p=Lp[j]+1;p<Lp[j+1];p++)
        x[Li[p]]-=Lx[p]*x[j];}

x[7] /= Lx[20]; // Peel
for(p = 21; p < 23; p++)
    x[Li[p]] -= Lx[p] * x[7];

for(px=4;px<reachSetSize;px++){
    j=reachSet[px];x[j]/=Lx[Lp[j]];
    for(p=Lp[j]+1;p<Lp[j+1];p++)
        x[Li[p]]-=Lx[p]*x[j];
}
```

Sympiler-optimized numerical code

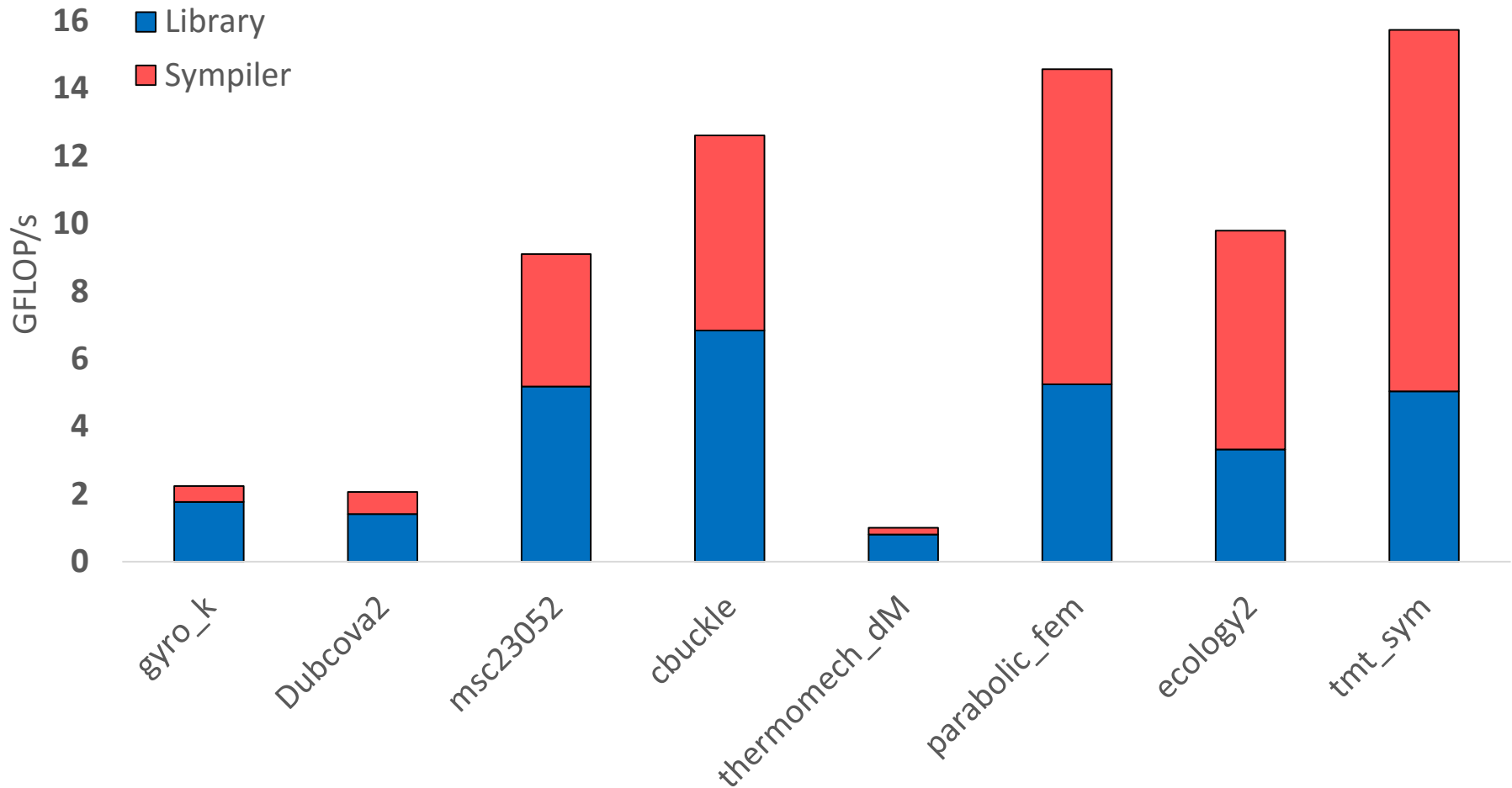
# CHOLSKY NUMERIC PERFORMANCE: STANDARD LIBRARY



The blue bar shows the performance for Eigen's numeric code for selected benchmarks.



# CHOLSKY NUMERIC PERFORMANCE: SYMPILER



The red bar shows Sympiler's added performance by generating optimized code for a static sparsity.

# OUTLINE

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# SYMPILER: A DOMAIN-SPECIFIC COMPILER FOR SPARSE DIRECT SOLVERS

**Sympiler** is a domain-specific compiler for generating high-performance code for direct sparse solvers

- The user provides the sparsity and type of solver as inputs.
- Sympiler decouples symbolic information from numerical computation at compile-time to generate optimized code.

**Example:** Find the solution to  $x$ ,  $Lx = b$  where  $L$  is sparse lower triangular matrix.

$$L: \{n, Lp, Li, Lx\}$$

$$\begin{bmatrix} 1 & & & & & & & & & \\ & 2 & & & & & & & & \\ & \cdot & 3 & & & & & & & \\ & & & 4 & & & & & & \\ & & & \cdot & 5 & & & & & \\ & \cdot & \cdot & \cdot & \cdot & 6 & & & & \\ & & & & & & 7 & & & \\ \cdot & & & & & & \cdot & \cdot & 8 & \\ & & \cdot & \cdot & \cdot & & & & \cdot & 9 \\ \cdot & & & & & & & \cdot & \cdot & 10 \end{bmatrix} * \begin{bmatrix} x_1 \\ x_2 \\ x_3 \\ x_4 \\ x_5 \\ x_6 \\ x_7 \\ x_8 \\ x_9 \\ x_{10} \end{bmatrix} = \begin{bmatrix} b_1 \\ \cdot \\ b_2 \end{bmatrix}$$

# SOLVING A SPARSE TRIANGULAR SYSTEM

Solve  $Lx=b$  with  $L$  unit lower triangular;  $L, x, b$  are sparse.

```
 $x = b$   
for  $j = 0$  to  $n - 1$  do  
  if  $x_j \neq 0$   
    for each  $i > j$  for which  $l_{ij} \neq 0$  do  
       $x_i = x_i - l_{ij}x_j$ 
```

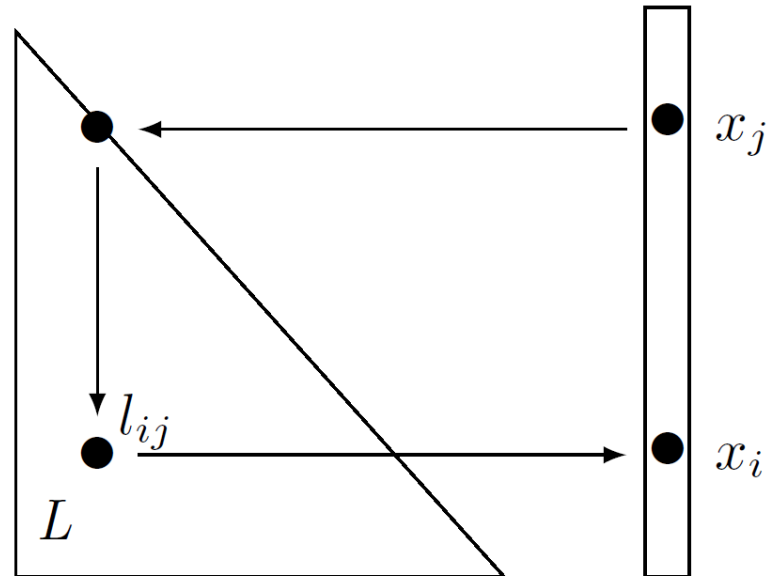
if  $b_i \neq 0$  then  $x_i \neq 0$

if  $x_j \neq 0$  and  $\exists i (l_{ij} \neq 0)$   
then  $x_i \neq 0$

start with pattern  $\beta$  of  $b$

graph  $L$ : edge  $(j, i)$  if  $l_{ij} \neq 0$

$\chi = \text{Reach}_L(\beta)$

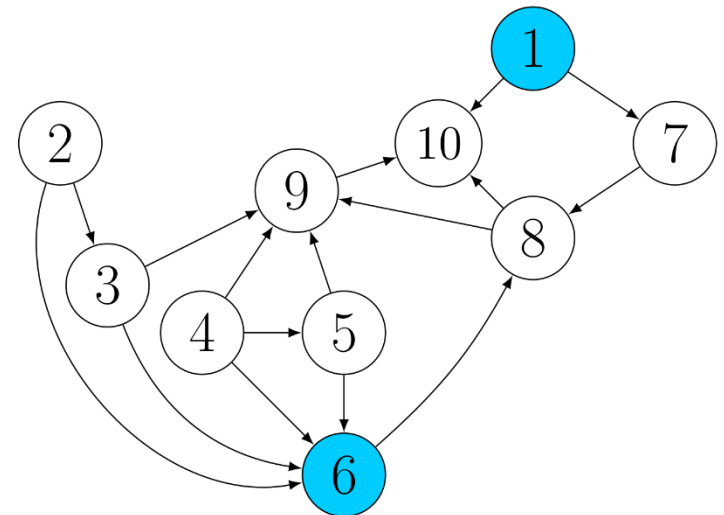


# SYMBOLIC ANALYSIS IN SPARSE TRIANGULAR SYSTEM SOLVER

L: {n,Lp,Li,Lx}

$$\begin{bmatrix}
 1 & & & & & & & & & \\
 & 2 & & & & & & & & \\
 & \bullet & 3 & & & & & & & \\
 & & & 4 & & & & & & \\
 & & & \bullet & 5 & & & & & \\
 & \bullet & \bullet & \bullet & \bullet & 6 & & & & \\
 & \bullet & & & & & 7 & & & \\
 & & & & & \bullet & \bullet & 8 & & \\
 & & \bullet & \bullet & \bullet & & & \bullet & 9 & \\
 & \bullet & & & & & & \bullet & \bullet & 10
 \end{bmatrix}
 *
 \begin{bmatrix}
 x \\
 x_1 \\
 x_2 \\
 x_3 \\
 x_4 \\
 x_5 \\
 x_6 \\
 x_7 \\
 x_8 \\
 x_9 \\
 x_{10}
 \end{bmatrix}
 =
 \begin{bmatrix}
 b \\
 \bullet \\
 \\
 \\
 \\
 \bullet \\
 \\
 \\
 \\
 \\
 \end{bmatrix}$$

Dependence Graph ( $DG_L$ )



# SYMBOLIC ANALYSIS IN SPARSE TRIANGULAR SYSTEM SOLVER

$L: \{n, Lp, Li, Lx\}$

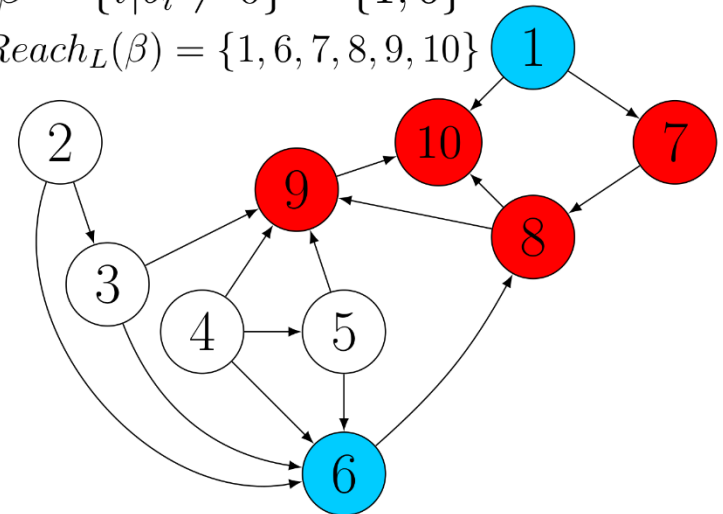
$$\begin{bmatrix}
 \textcolor{red}{1} & & & & & & & & & \\
 & 2 & & & & & & & & \\
 & \bullet & 3 & & & & & & & \\
 & & & 4 & & & & & & \\
 & & & \bullet & 5 & & & & & \\
 & \bullet & \bullet & \bullet & \bullet & \textcolor{red}{6} & & & & \\
 \textcolor{red}{\bullet} & & & & & & \textcolor{red}{7} & & & \\
 & & & & & \textcolor{red}{\bullet} & \textcolor{red}{\bullet} & \textcolor{red}{8} & & \\
 & \bullet & \bullet & \bullet & & & & \textcolor{red}{\bullet} & \textcolor{red}{9} & \\
 \textcolor{red}{\bullet} & & & & & & & \textcolor{red}{\bullet} & \textcolor{red}{\bullet} & \textcolor{red}{10}
 \end{bmatrix}
 *
 \begin{bmatrix}
 x \\
 x_1 \\
 x_2 \\
 x_3 \\
 x_4 \\
 x_5 \\
 x_6 \\
 x_7 \\
 x_8 \\
 x_9 \\
 x_{10}
 \end{bmatrix}
 =
 \begin{bmatrix}
 b \\
 \textcolor{cyan}{\bullet} \\
 \\
 \\
 \\
 \textcolor{cyan}{\bullet} \\
 \\
 \\
 \\
 \\
 \end{bmatrix}$$

Depth First Search (DFS)

Dependence Graph ( $DG_L$ )

$$\beta = \{i | b_i \neq 0\} = \{1, 6\}$$

$$Reach_L(\beta) = \{1, 6, 7, 8, 9, 10\}$$



# TRANSFORMING THE SPARSE CODE

## Standard Code

```
for (j=0; j<n; j++){  
    x[j]/=Lx[Lp[j]];   
  
    for (p=Lp[j]+1; p<Lp[j+1]; p++)  
        x[Li[p]]-=Lx[p]*x[j];  
}
```

# TRANSFORMING THE SPARSE CODE

## Standard Code

```
for (j=0; j<n; j++){
```

```
    x[j]/=Lx[Lp[j]];
```

Reachset = {6,1,7,8,9, 10}

```
    for (p=Lp[j]+1; p<Lp[j+1]; p++)  
        x[Li[p]]-=Lx[p]*x[j];  
}
```



## Symbolically-Guided Code

```
for (px=0; px<RSsize; px++) {
```

```
    j=reachset[px];
```

```
    x[j]/=Lx[Lp[j]]
```

```
    p=Lp[j]+1;
```

```
    for (; p<Lp[j+1]; p++)
```

```
        x[Li[p]]-=Lx[p]*x[j];
```

```
}
```



# TRANSFORMING THE SPARSE CODE

## Symbolically-Guided Code

```
for (px=0; px<RSsize; px++) {  
    j=reachset[px];  
    x[j]/=Lx[Lp[j]]  
    p=Lp[j]+1;  
    for (; p<Lp[j+1]; p++)  
        x[Li[p]]-=Lx[p]*x[j];  
}
```

# TRANSFORMING THE SPARSE CODE

## Symbolically-Guided Code

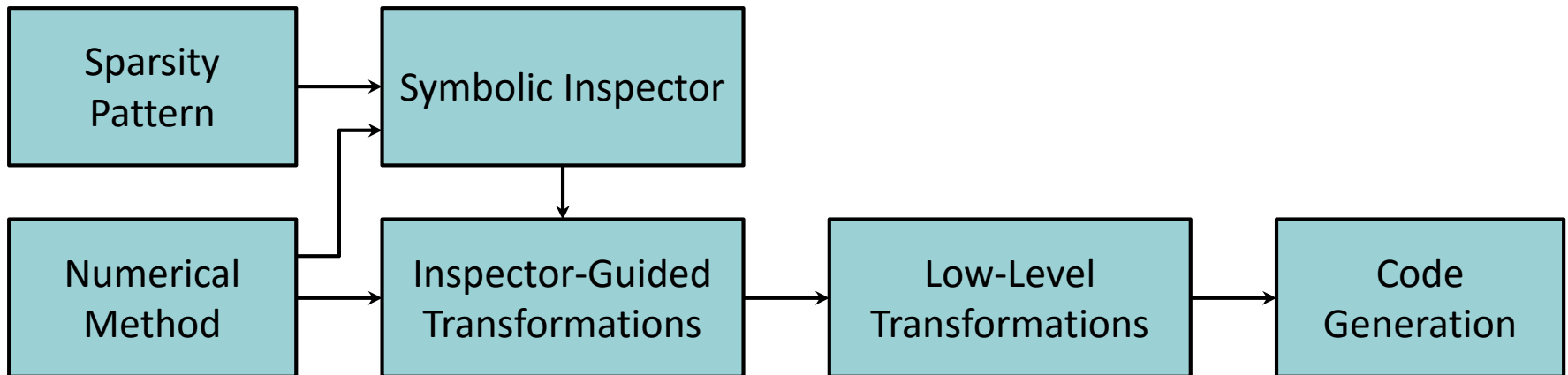
```
for (px=0; px<RSsize; px++) {  
    j=reachset[px];  
    x[j]/=Lx[Lp[j]]  
    p=Lp[j]+1;  
    for (; p<Lp[j+1]; p++)  
        x[Li[p]]-=Lx[p]*x[j];  
}
```

Peel

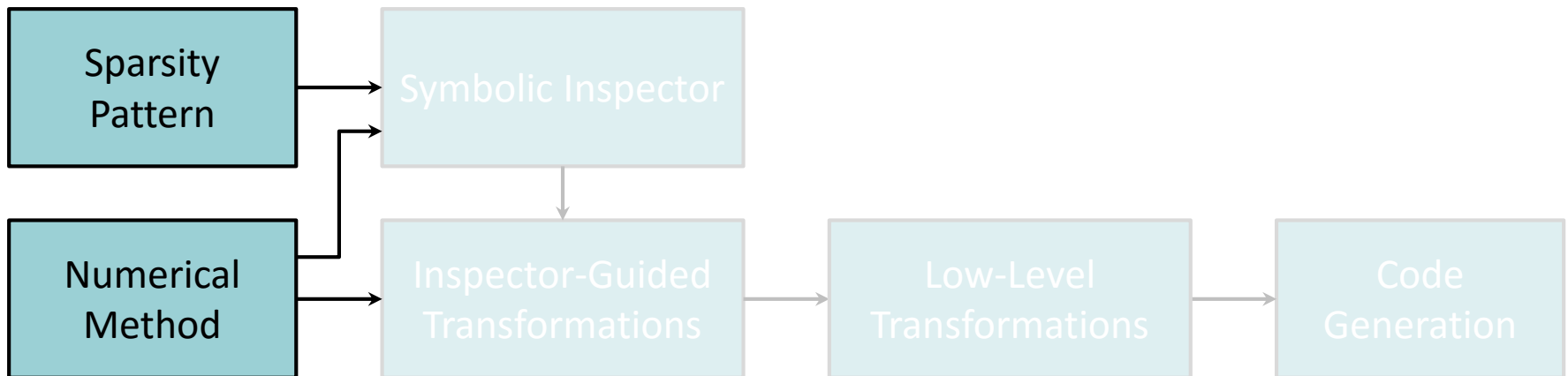
## Optimized Code

```
x=b;  
x[0] /= Lx[0]; // Peel col 0  
for(p = 1; p < 3; p++)  
    x[Li[p]] -= Lx[p] * x[0];  
  
for(px=1;px<3;px++){  
    j=reachSet[px];x[j]/=Lx[Lp[j]];  
    for(p=Lp[j]+1;p<Lp[j+1];p++)  
        x[Li[p]]-=Lx[p]*x[j];}  
  
x[7] /= Lx[20]; // Peel col 7  
for(p = 21; p < 23; p++)  
    x[Li[p]] -= Lx[p] * x[7];  
  
for(px=4;px<reachSetSize;px++){  
    j=reachSet[px];x[j]/=Lx[Lp[j]];  
    for(p=Lp[j]+1;p<Lp[j+1];p++)  
        x[Li[p]]-=Lx[p]*x[j];  
}
```

# SYMPILE INTERNALS



# INPUTS TO SYMPILER



# AN EXAMPLE INPUT REPRESENTATION OF THE SPARSE TRIANGULAR SYSTEM SOLVER

```
int main() {  
  Sparse L(Float(64),"L.mtx");  
  Sparse rhs(Float(64),"RHS.mtx");  
  
  Triangular trns(L,rhs);  
  trns.sympile_to_c("triang");  
}
```

Input sparsity patterns

Numerical method

Sparsity  
Pattern

Symbolic  
Inspector

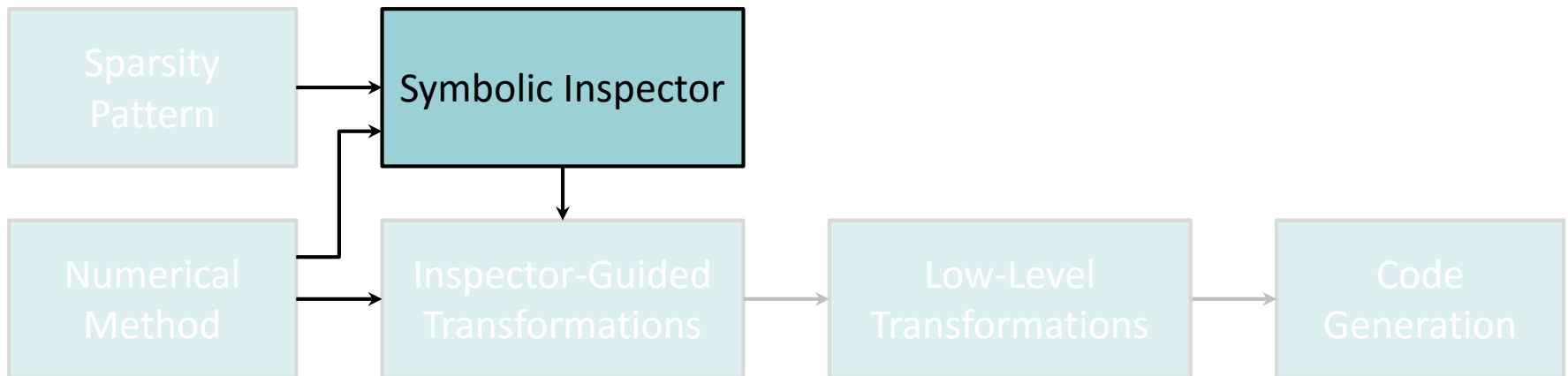
Numerical  
Method

Inspector-Guided  
Transformations

Low-Level  
Transformations

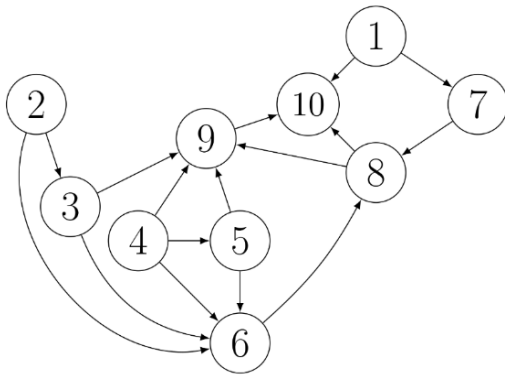
Code  
Generation

# SYMBOLIC INSPECTION

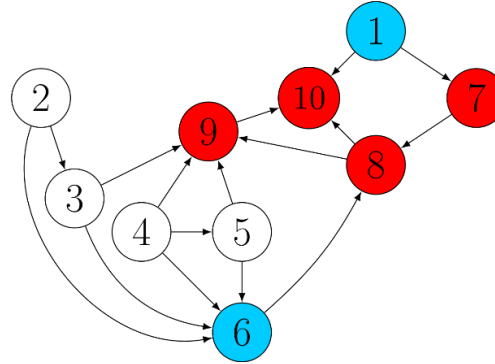


# THE SYMBOLIC INSPECTOR

Symbolic inspector creates an **inspection graph** from the given sparsity pattern and traverses it during inspection using a specific **inspection strategy**. The result of the inspection is the **inspection set**.



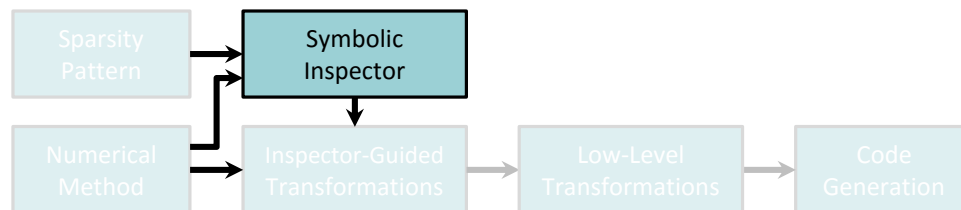
Inspection Graph:  
 $DG_L$



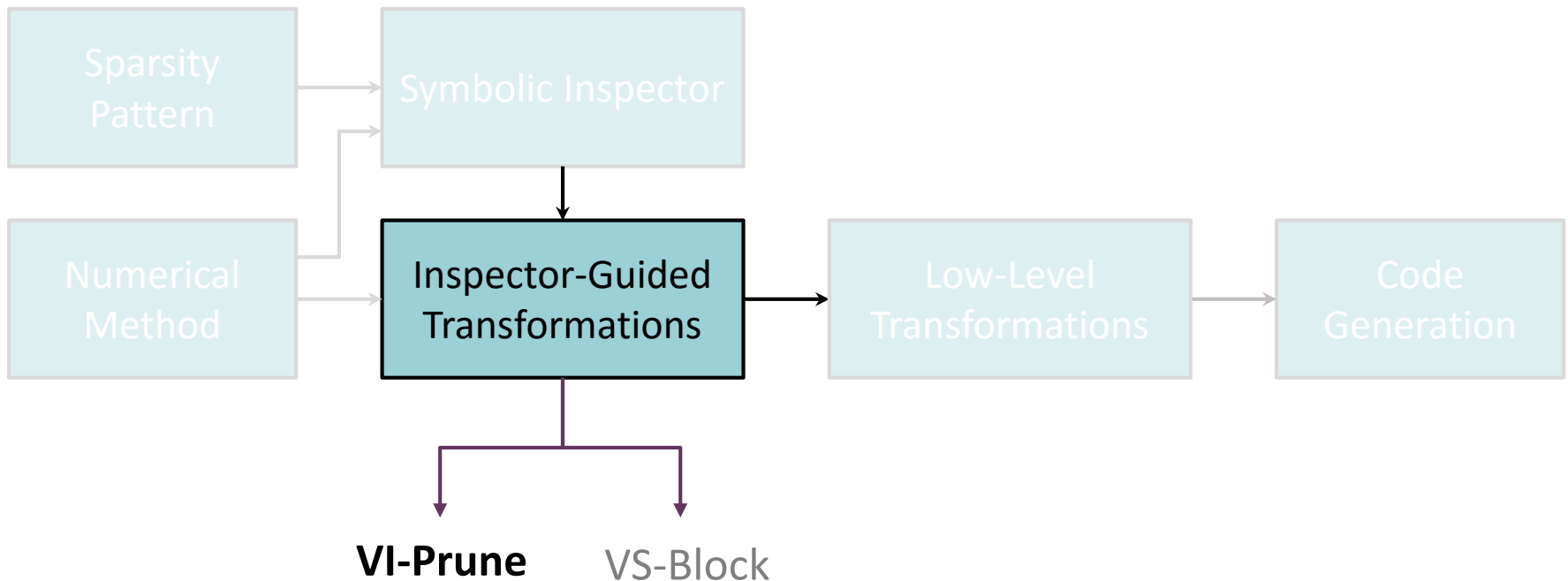
Inspection Strategy:  
DFS

$$Reach_L(\beta) = \{1, 6, 7, 9, 10\}$$

Inspection Set:  
Reachset



# INSPECTOR-GUIDED TRANSFORMATIONS





# INSPECTOR-GUIDED TRANSFORMATIONS: VI-PRUNE

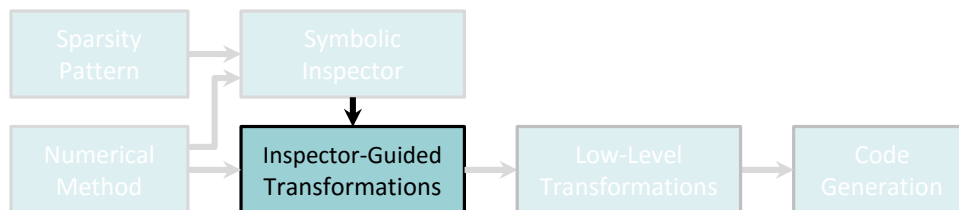
**Variable Iteration Space Pruning (VI-Prune)** prunes the iteration space of a loop using information about the sparse computation.

```
for(I1){  
  ...  
  for(Ik < m) {  
    ...  
    for(In(Ik, ..., In-1)) {  
      a[idx(I1, ..., Ik, ..., In)];  
    }  
  }  
}
```

Original code

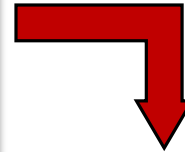
```
for(I1){  
  ...  
  for(Ip < pruneSetSize) {  
    I'k = pruneSet[Ip];  
    ...  
    for(In(I'k, ..., In-1)) {  
      a[idx(I1, ..., I'k, ..., In)];  
    }  
  }  
}
```

Transformed with VI-Prune



# THE INITIAL ABSTRACT SYNTAX TREE (AST) WITH ANNOTATIONS

```
int main() {  
  Sparse L(Float(64), "L.mtx");  
  Sparse rhs(Float(64), "RHS.mtx");  
  
  Triangular trns(L, rhs);  
  trns.sympile_to_c("triang");  
}
```



VI-Prune

```
for sol.j0 in 0..Lsp.n
```

VS-Block

```
x[bspj0] /= Lx[Lsp.diagj0]);
```

VS-Block

```
  for sol.j1 in Lsp.colj0..Lsp.colj0+1  
    x[Lsp.rowj1] -= Lx[j1]*x[bspj0];
```

Annotations for **Inspector-Guided transformations**

Sparsity  
Pattern

Symbolic  
Inspector

Numerical  
Method

Inspector-Guided  
Transformations

Low-Level  
Transformations

Code  
Generation

# VI-PRUNE TRANSFORMATION FOR TRIANGULAR SOLVE

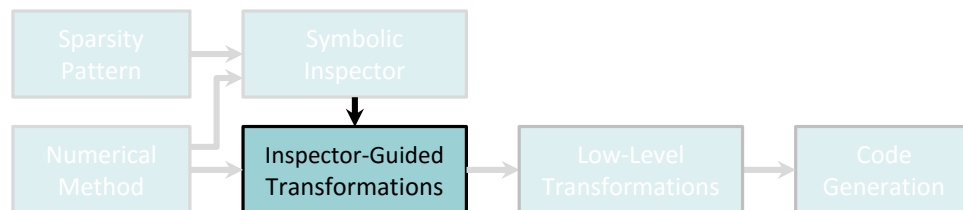
## VI-Prune

```
for sol.j0 in 0..Lsp.n
  VS-Block
  x[bspj0] /= Lx[Lsp.diagj0]];
  VS-Block
  for sol.j1 in Lsp.colj0..Lsp.colj0+1
    x[Lsp.rowj1] -= Lx[j1]*x[bspj0];
```

VI-Prune  
transformation

```
peel(0,3)
for sol.p0 in 0..pruneSetSize
  j0=pruneSetp0;
  x[bspj0]/=Lx[Lsp.diag(j0));
  vec(0,3)
  for sol.j1 in Lsp.colj0..Lsp.colj0+1
    x[Lsp.rowj1]-=Lx[j1]*x[bspj0];
```

```
s0=pruneSet0;
x[bsps0]/=Lx[Lsp.diag(s0)];
for sol.j1 in Lsp.cols0..Lsp.cols0+1
  x[Lsp.rowj1]-=Lx[j1]*x[bsps0];
for sol.p0 in 0..pruneSetSize
  j0=pruneSetp0;
  x[bspj0]/=Lx[Lsp.diag(j0)];
  for sol.j1 in Lsp.colj0..Lsp.colj0+1
    x[Lsp.rowj1]-=Lx[j1]*x[bspj0];
```



# LOW-LEVEL TRANSFORMATION FOR TRIANGULAR SOLVE

**VI-Prune**

```
for sol.j0 in 0..Lsp.n
```

**VS-Block**

```
x[bspj0] /= Lx[Lsp.diagj0]);
```

**VS-Block**

```
for sol.j1 in Lsp.colj0..Lsp.colj0+1
```

```
x[Lsp.rowj1] -= Lx[j1]*x[bspj0];
```

```
peel(0,3)
```

```
for sol.p0 in 0..pruneSetSize
```

```
j0=pruneSetp0;
```

```
x[bspj0] /= Lx[Lsp.diag(j0)];
```

```
vec(0,3)
```

```
for sol.j1 in Lsp.colj0..Lsp.colj0+1
```

```
x[Lsp.rowj1] -= Lx[j1]*x[bspj0];
```

Low-level transformation: Peel(0,3)

```
s0=pruneSet0;
```

```
x[bsps0] /= Lx[Lsp.diag(s0)];
```

```
for sol.j1 in Lsp.cols0..Lsp.cols0+1
```

```
x[Lsp.rowj1] -= Lx[j1]*x[bsps0];
```

```
for sol.p0 in 0..pruneSetSize
```

```
j0=pruneSetp0;
```

```
x[bspj0] /= Lx[Lsp.diag(j0)];
```

```
for sol.j1 in Lsp.colj0..Lsp.colj0+1
```

```
x[Lsp.rowj1] -= Lx[j1]*x[bspj0];...
```

Sparsity  
Pattern

Symbolic  
Inspector

Numerical  
Method

Inspector-Guided  
Transformations

Low-Level  
Transformations

Code  
Generation

# CODE-GENERATION FOR TRIANGULAR SOLVE

```

s0=pruneSet0;
x[bsps0]/=Lx[Lsp.diag(s0)];
for sol.j1 in Lsp.cols0..Lsp.cols0+1
  x[Lsp.rowj1]-=Lx[j1]*x[bsps0];
for sol.p0 in 0..pruneSetSize
  j0=pruneSetp0;
  x[bspj0]/=Lx[Lsp.diag(j0)];
  for sol.j1 in Lsp.colj0..Lsp.colj0+1
    x[Lsp.rowj1]-=Lx[j1]*x[bspj0];...
    
```

Final C code generation

```

x=b;
x[0] /= Lx[0]; // Peel col 0
for(p = 1; p < 3; p++)
  x[Li[p]] -= Lx[p] * x[0];

for(px=1;px<3;px++){
  j=reachSet[px];x[j]/=Lx[Lp[j]];
  for(p=Lp[j]+1;p<Lp[j+1];p++)
    x[Li[p]]-=Lx[p]*x[j];}

x[7] /= Lx[20]; // Peel col 7
for(p = 21; p < 23; p++)
  x[Li[p]] -= Lx[p] * x[7];

for(px=4;px<reachSetSize;px++){
  j=reachSet[px];x[j]/=Lx[Lp[j]];
  for(p=Lp[j]+1;p<Lp[j+1];p++)
    x[Li[p]]-=Lx[p]*x[j];}
    
```

Sparsity  
Pattern

Symbolic  
Inspector

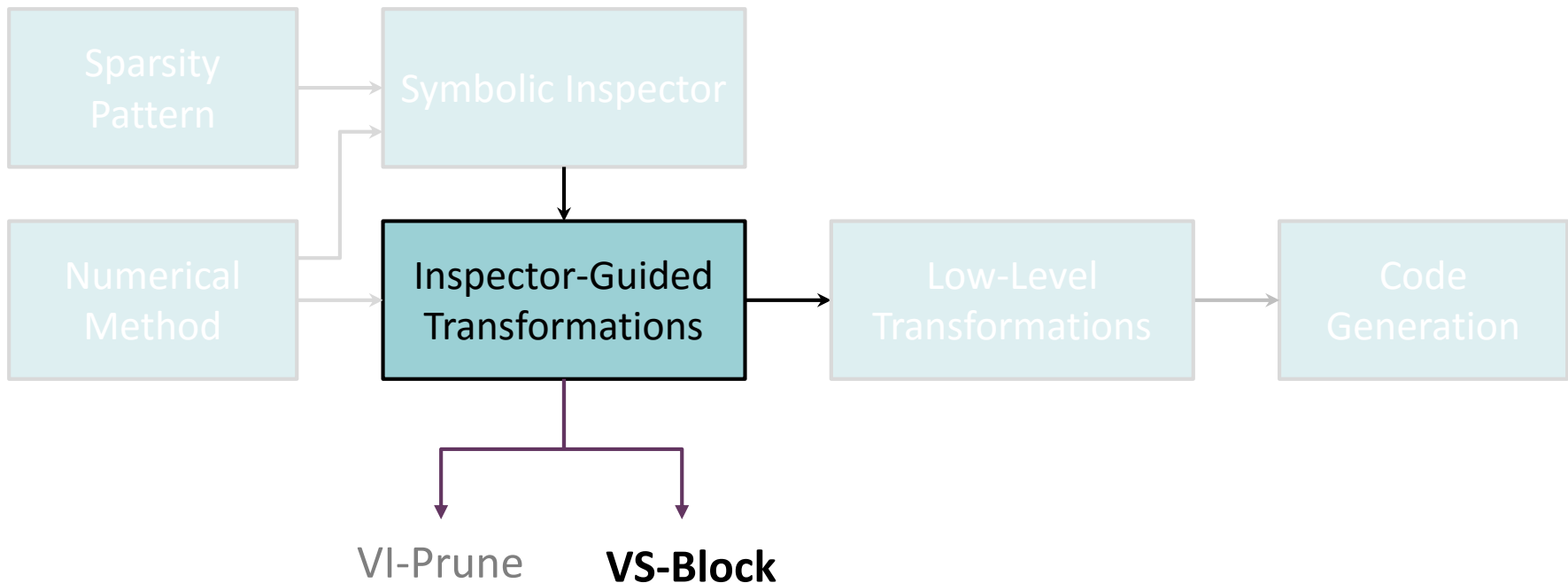
Numerical  
Method

Inspector-Guided  
Transformations

Low-Level  
Transformations

Code  
Generation

# INSPECTOR-GUIDED TRANSFORMATIONS



# INSPECTOR-GUIDED TRANSFORMATIONS: VS-BLOCK

**2D Variable-Sized Blocking (VS-Block)** converts a sparse code to a set of non-uniform dense subkernels. VS-Block identifies supernodes.

The unstructured computations and inputs in sparse kernels make blocking optimizations challenging: variable block sizes, algorithm change for diagonal operations, and the block elements may not be in consecutive locations.

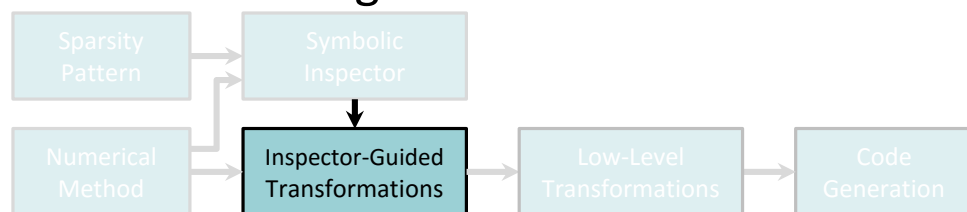
```
for(I) {  
  for(J) {  
    B[idx1(I,J)] op1= a[idx2(I,J)];  
  }  
}
```



```
for(b < blockSetSize) {  
  for(J1 < blockSet[b].x) {  
    for(J2 < blockSet[b].y) {  
      B[idx1(b,J1,J2)] op1 =  
        A[idx2(b,J1,J2)];  
    }  
  }  
}
```

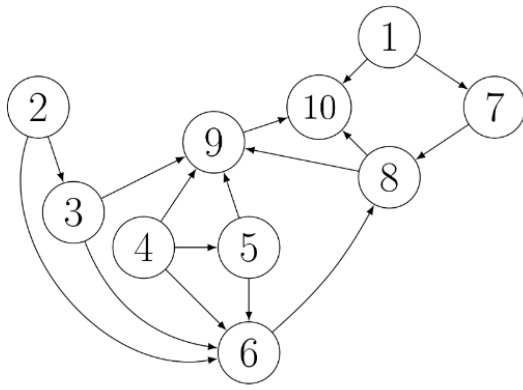
Original code

Transformed with VS-Block

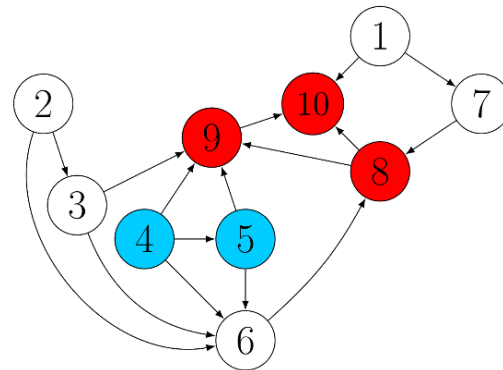


# VS-BLOCK FOR TRIANGULAR SOLVER

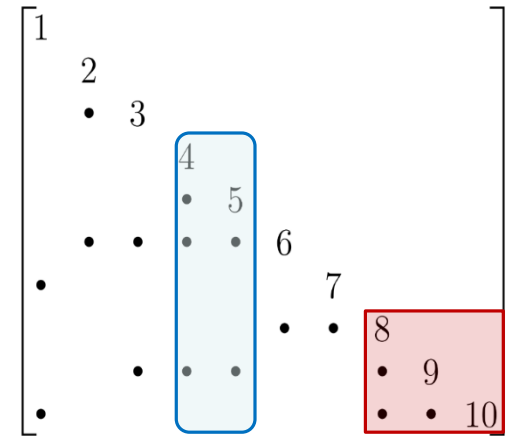
The Symbolic inspector detects supernodes (columns with similar sparsity patterns) and creates dense subkernels.



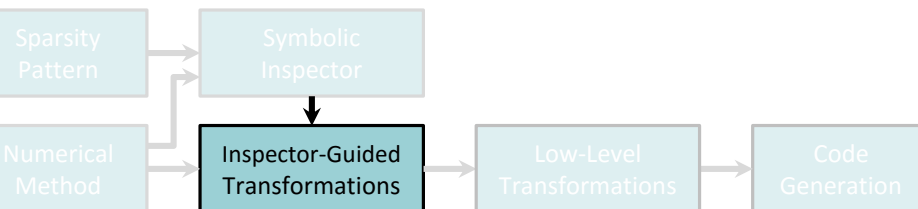
Inspection Graph:  
 $DG_L$



Inspection Strategy:  
Node equivalence



Inspection Set:  
Blockset





# OUTLINE

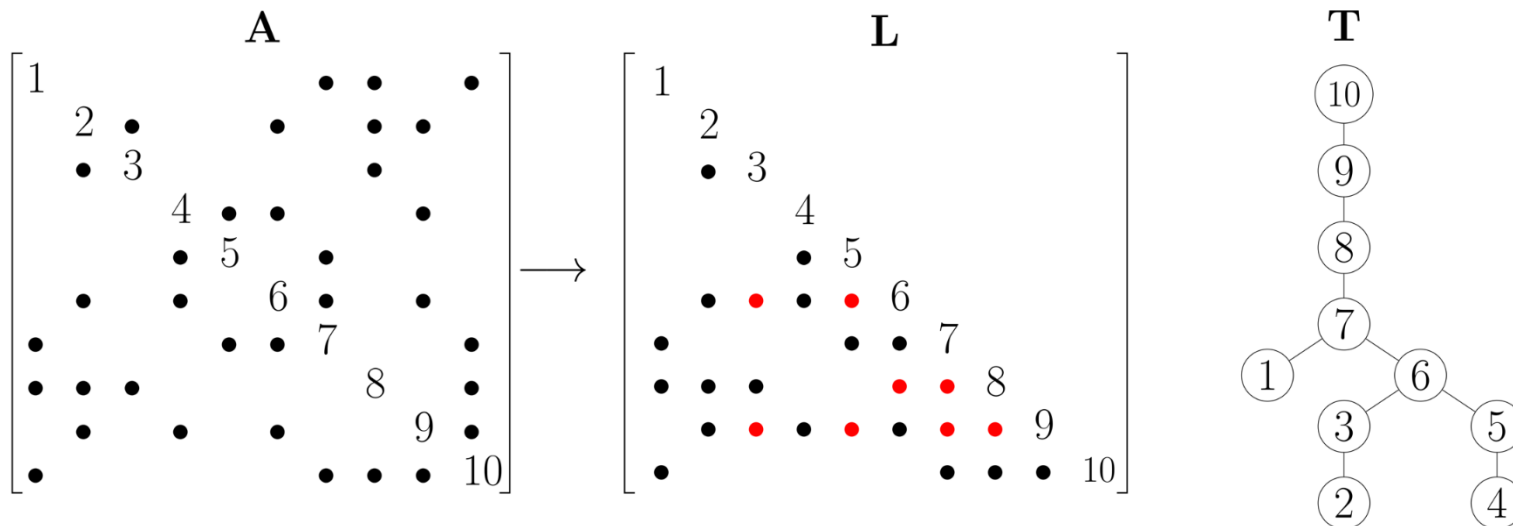
---

- Overview
- Sympiler: A code generator for optimizing sparse matrix methods
  - Sympiler internals (input, inspection, transformation, codegen) with the triangular system solver example
  - Sympiler for Cholesky factorization
- Conclusion

# CASE STUDY: CHOLESKY FACTORIZATION

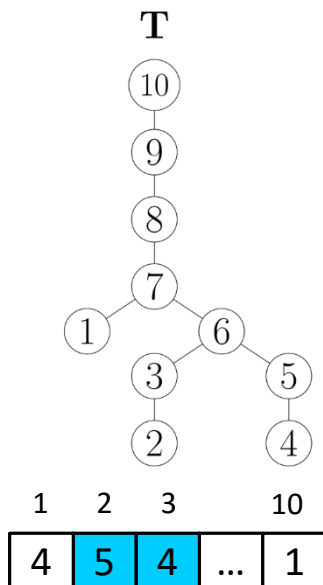
**Cholesky factorization** is commonly used in direct solvers and is used to precondition iterative solvers.

**The elimination tree (T)** is one of the most important graph structures used in the symbolic analysis of sparse factorization algorithms.

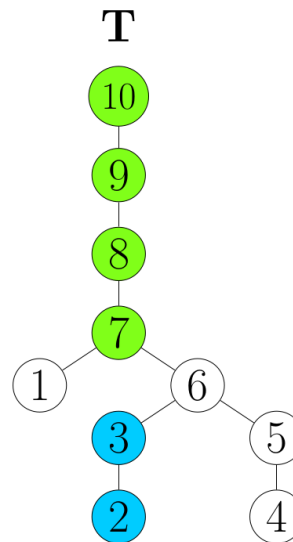


# SYMBOLIC INSPECTION FOR VS-BLOCK IN CHOLESKY

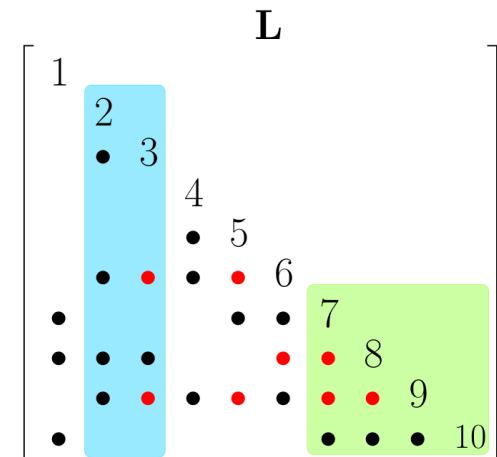
Supernodes in VS-Block for Cholesky are found using the L sparsity pattern and the elimination tree.



Inspection Graph:  
Elimination tree  
and column count



Inspection Strategy:  
Uptraversal



Inspection Set:  
Blockset

# THE VS-BLOCK TRANSFORMATION IN CHOLESKY

```
for(column j = 0 to n){  
  f = A(:,j);  
  // Update  
  for(r=0 to j-1 && L(j,1)!=0){  
    f -= L(j:n,r) * L(j,r);  
  }  
  // Diagonal  
  L(k,k) = sqrt(f(k));  
  // Off-diagonal  
  for(off-diagonal elements in f){  
    L(k+1:n,k) = f(k+1:n) / L(k,k);  
  }  
}
```

VS-Block

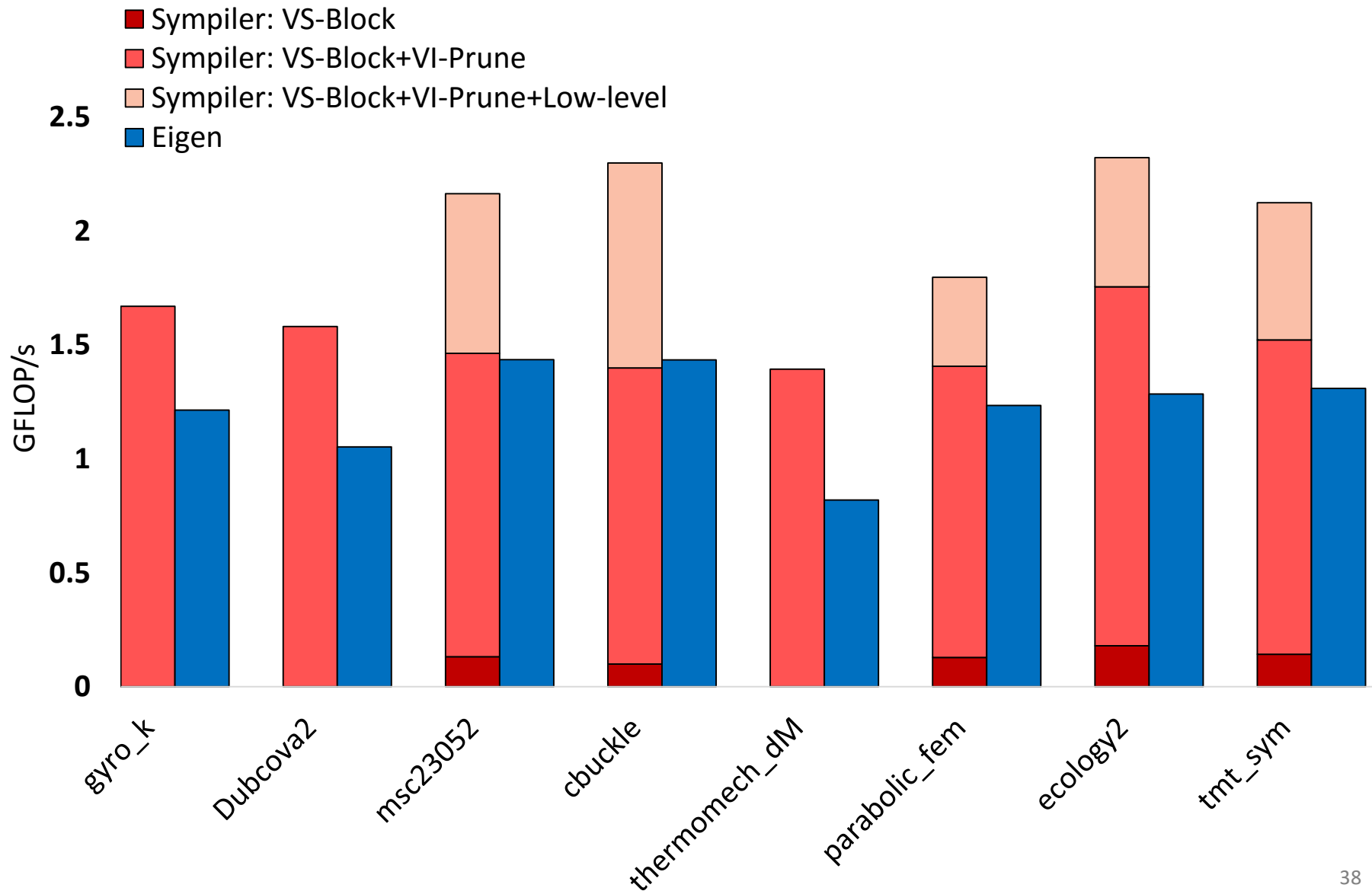
```
for(column j = 1 to blockSetNo){  
  lb = blockSet[j-1];  
  ub = blockSet[j];  
  f = A(:,lb:ub)  
  // Update  
  for(block r=0 to j-1 &&  
    L(j,r)!=0){  
    f -= L(lb:n,r) * L'(j,r);  
  }  
  // Diagonal  
  L(lb:ub,lb:ub) =  
    Cholesky_dense(f(lb:ub));  
  // Off-diagonal  
  for(off-diagonal elements in f){  
    L(ub+1:n,lb:ub) =  
      triangular_dense(f(u+1:n,lb:ub),  
        L(lb:ub,lb:ub));  
  }  
}
```

# EXPERIMENTAL SETUP

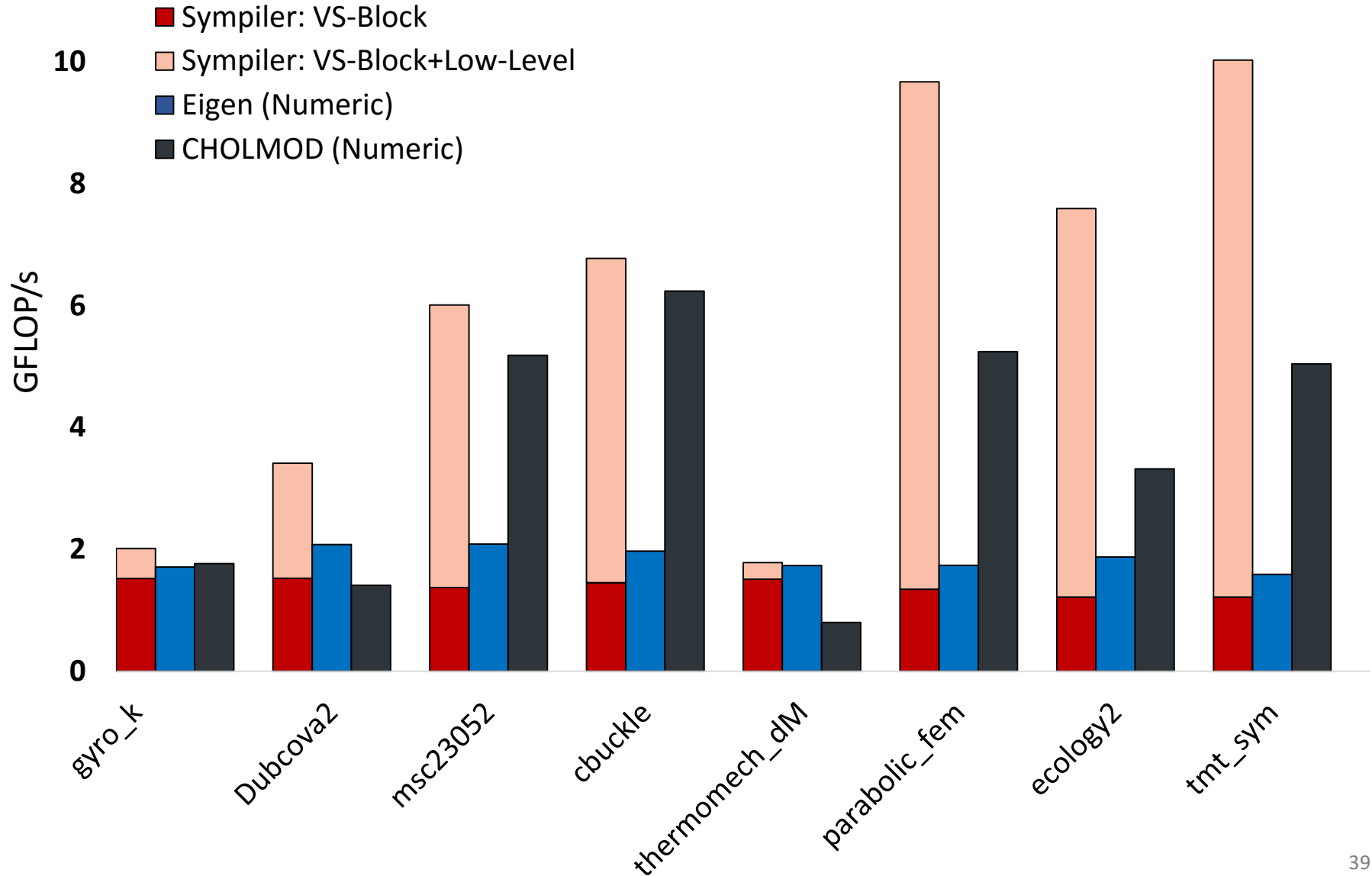
Numeric and symbolic times are compared separately where applicable. **Target processor:** Intel Core i7-5820K; **Benchmarks:** University of Florida repository

Name	Application	Order ( $10^3$ )	Non-zeros ( $10^6$ )
gyro_k	duplicate model reduction problem	17.4	1.02
Dubcova2	2D/3D problem	65.0	1.03
msc23052	structural problem	23.1	1.14
cbuckle	shell buckling	13.7	0.677
thermomech_dM	thermal problem	204	1.42
parabolic_fem	computational fluid dynamics problem	526	3.67
ecology2	circuitscape	1000	5.00
tmt_sym	electromagnetics	727	5.08

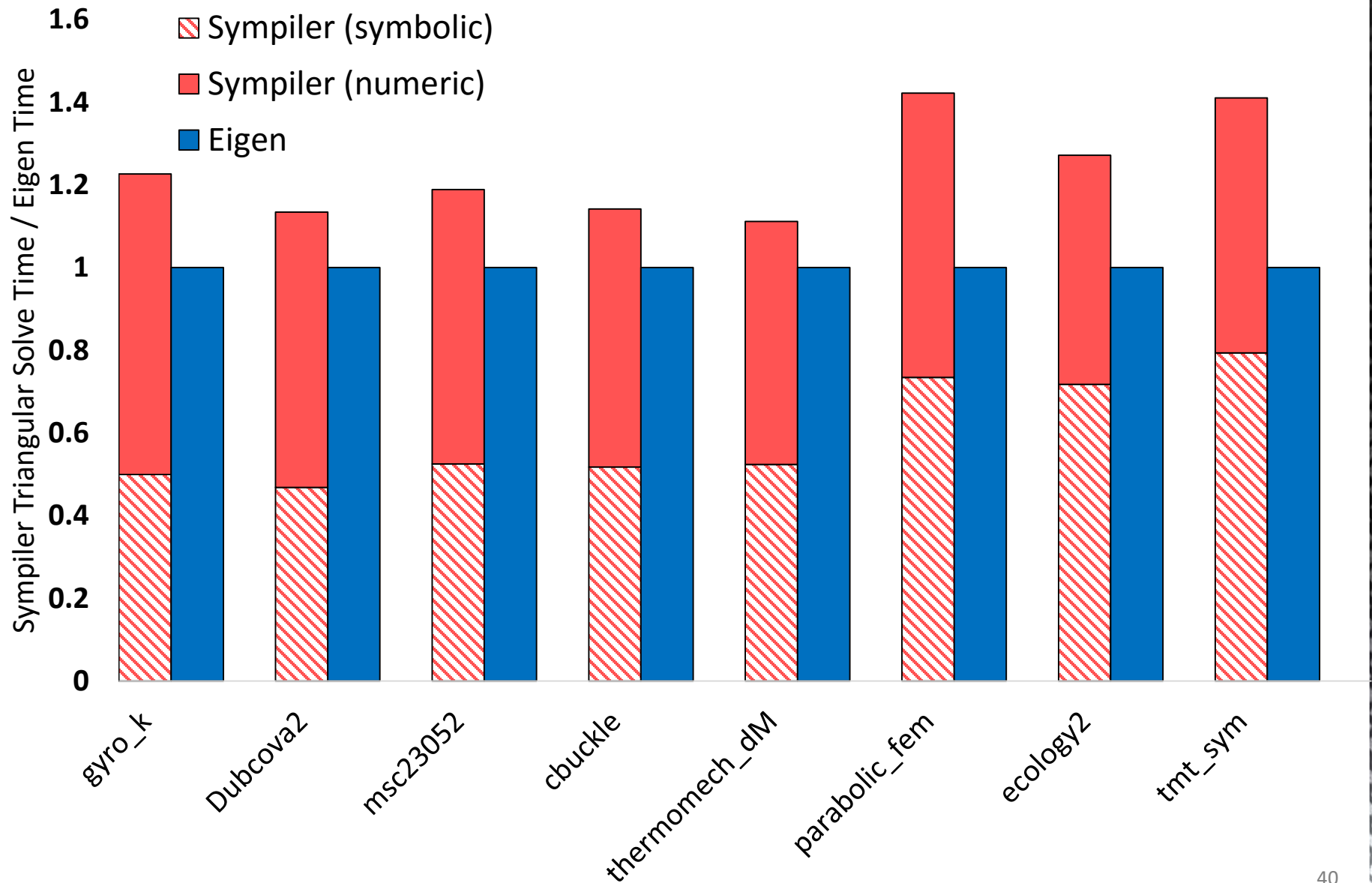
# TRIANGULAR SOLVER PERFORMANCE



# CHOLESKY PERFORMANCE

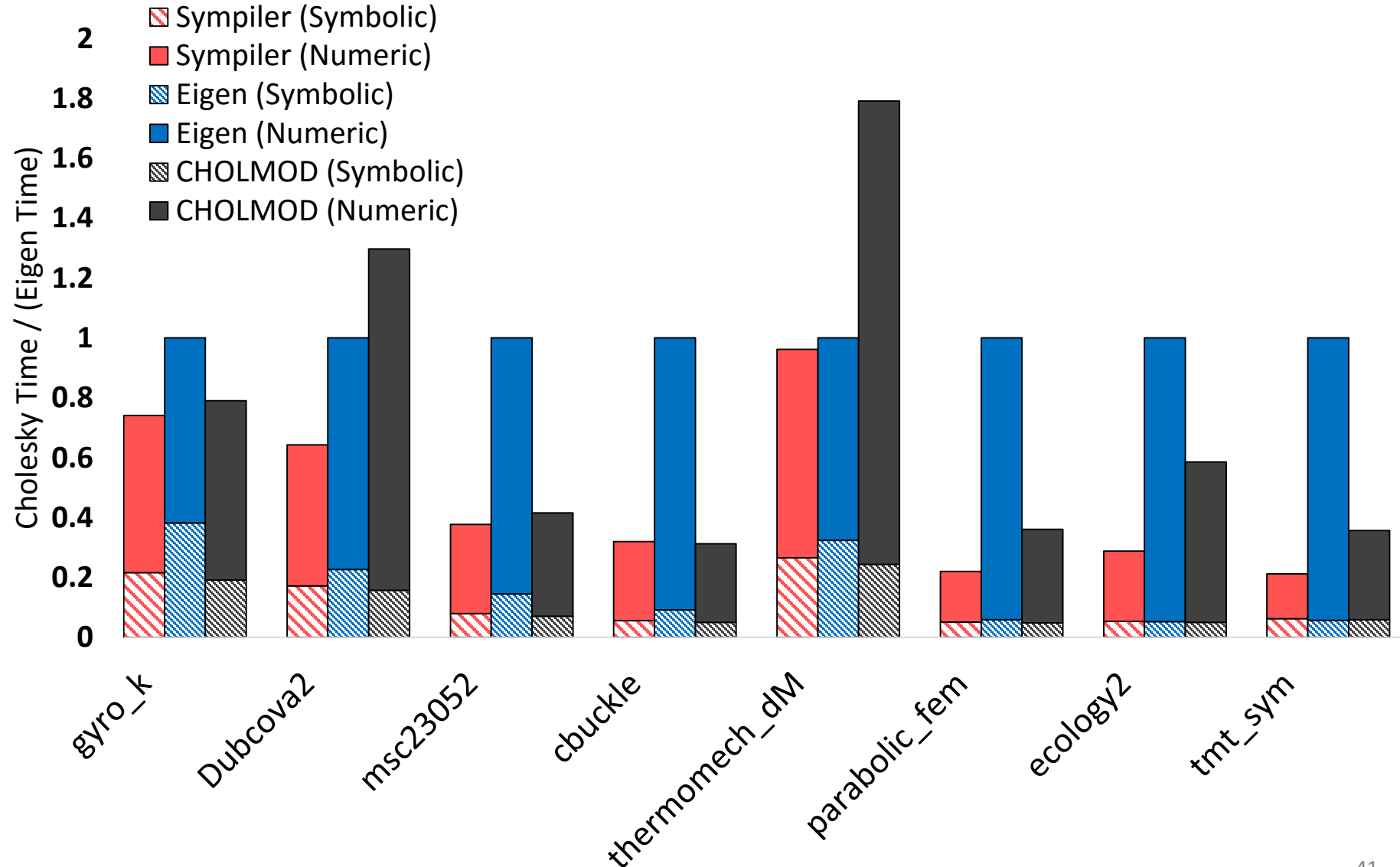


# SYMBOLIC ANALYSIS COST: TRIANGULAR SOLVER





# SYMBOLIC ANALYSIS COST: CHOLESKY



# OUTLINE

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- Overview
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# CONCLUSION

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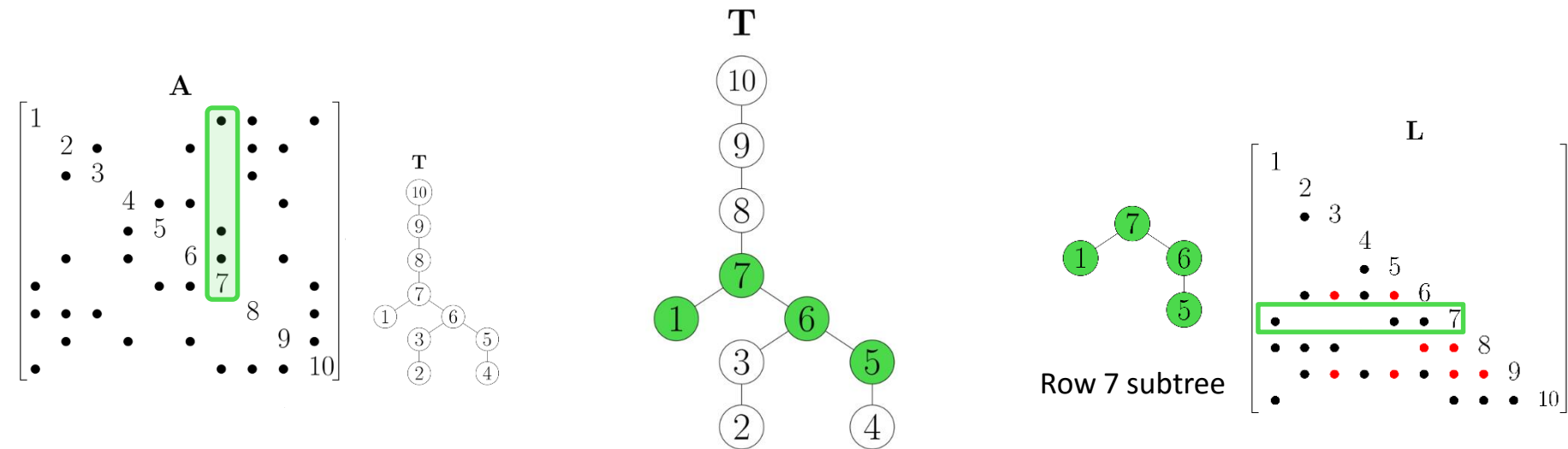
- Sympiler is a domain-specific code generator for transforming sparse matrix codes.
- It uses the information from symbolic analysis to apply a number of inspector-guided and low-level transformations.
- The Sympiler-generated code outperforms two state-of-the-art sparse libraries, Eigen and CHOLMOD.
- Sympiler source code is publicly available from:

***<https://github.com/sympiler/sympiler>***

Thank you

# SYMBOLIC INSPECTION FOR VI-PRUNE IN CHOLESKY

The Symbolic inspector uses the elimination tree and upper part of A to find row sparsity patterns to create the prune-set.



Inspection Graph:  
Elimination tree  
and upper A

Inspection Strategy:  
Single-node uptraversal

Inspection Set:  
Pruneset

# THE VI-PRUNE TRANSFORMATION IN CHOLESKY

```
for(column j = 0 to n){  
  f = A(:,j)  
  // Update  
  for(r=0 to j-1 && L(j,1)!=0){  
    f -= L(j:n,r) * L(j,r);  
  }  
  // Diagonal  
  L(k,k) = sqrt(f(k));  
  // Off-diagonal  
  for(off-diagonal elements in f){  
    L(k+1:n,k) = f(k+1:n) / L(k,k);  
  }  
}
```

VI-Prune

```
for(column j = 0 to n){  
  f = A(:,j)  
  // Update  
  for(every r in PruneSet){  
    f -= L(j:n,r) * L(j,r);  
  }  
  // Diagonal  
  L(k,k) = sqrt(f(k));  
  // Off-diagonal  
  for(off-diagonal elements in f){  
    L(k+1:n,k) = f(k+1:n) / L(k,k);  
  }}  
}
```